Code No: 09A50402

R09

## JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, HYDERABAD B. Tech III Year I Semester Examinations, May/June – 2013

Computer Organization (Common to ECE, ETM)

Time: 3 hours

Max. Marks: 75

## Answer any five questions All questions carry equal marks

			JAN.					
1.a)	Explain various system.	ous types	of repres	senting signe	d num	bers in the fi	xed point	number
b)		11					17-20-c	
b)	Define the fol	_	1	• , 2015		•••\ T	. •	(C. 20 - 2)
	i) Assembler		11)	interpreter		iii) Executio	n time.	[15]
2.a)	Drove and over	love the he		for ittle one o	1.10200		- 1	
	Draw and explain the hardware for integer addition and subtraction.  Explain about:							
b)					2000 C			(1.67
	i) Instruction	codes	(11)	Computer in	istructi	on set.		[15]
3.	What are the different local in Assistance 1 is in							
3.	What are the different branching techniques used in micro programmed control unit? Explain with suitable example. [15]							
	unit? Explain	with suital	ole exan	nple.	***			[15]
					and the same		30.00	
4.	Draw and exp	plain the Fl	owchart	for floating	point n	nultiplication		[15]
		1						
5.	Explain about different types of cache memory techniques with neat sketches.							
								[15]
6.a)	What are the	different m	ethods	used for I/O	address	sing?	834	
b)	Explain how processor responds to an Interrupt.							[15]
	. Aller		1				44.00	[]
7.a)	What is an ari	thmetic pir	peline?				1015 ¥	
b)	Explain the arithmetic pipeline for floating point adder/subtractor with the help of							
701	one example.	realistic p	. Pormo	or rigating p	Onic ac	idoi/Subirdoto	vitti tiic	[15]
334		500						
8.	Explain the va	rious type	s of inte	r connection	structu	ires.		[15]

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