

R09

Code No: 09A50402

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY, HYDERABAD

B. Tech III Year I Semester Examinations, May/June – 2013

Computer Organization

(Common to ECE, ETM)

Time: 3 hours

Max. Marks: 75

Answer any five questions

All questions carry equal marks

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- 1.a) Explain various types of representing signed numbers in the fixed point number system.
- b) Define the following:
  - i) Assembler
  - ii) interpreter
  - iii) Execution time. [15]
- 2.a) Draw and explain the hardware for integer addition and subtraction.
- b) Explain about:
  - i) Instruction codes
  - ii) Computer instruction set. [15]
3. What are the different branching techniques used in micro programmed control unit? Explain with suitable example. [15]
4. Draw and explain the Flowchart for floating point multiplication. [15]
5. Explain about different types of cache memory techniques with neat sketches. [15]
- 6.a) What are the different methods used for I/O addressing?
- b) Explain how processor responds to an Interrupt. [15]
- 7.a) What is an arithmetic pipeline?
- b) Explain the arithmetic pipeline for floating point adder/subtractor with the help of one example. [15]
8. Explain the various types of inter connection structures. [15]

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